

July 2013

Monday	Tuesday	Wednesday	Thursday	Friday	Saturday	Sunday
1	2	3	4	5	6	7
8	9	10	11	12	13	14
15	16	17	18	19	20	21
22	23	24	25	26	27 1 st complete results of the 4-LUT comparison	28
29 LUT/AIC finished & optimized (frozen)	30	31 Final results of the 4-LUT comparison				

August 2013

Monday	Tuesday	Wednesday	Thursday	Friday	Saturday	Sunday
			1	2 First sequential solution tested	3	4
5 Second sequential solution tested	6	7	8 Sequential problem solved (?)	9	10	11
12 LUT cluster finished & optimized	13	14	15 Complete experimental setup -preliminary numb.	16	17	18
19 AIC cluster finished & optimized	20	21	22	23	24	25
26 Final results (FPGA'12 paper re-implemented)	27	28	29	30	31	

September 2013

Monday	Tuesday	Wednesday	Thursday	Friday	Saturday	Sunday
						1
2 FPL'13	3 FPL'13	4 FPL'13	5 FPL'13	6 FPL'13	7	8
9	10	11	12	13	14	15
16	17	18	19	20 FPGA'14 deadline	21	22
23	24	25	26	27 FPGA'14 extended deadline	28	29
30						